

Raspberry to LQ0925B5DW Pins

Pin GPIO	Pin . LQ0925B5DW0	Symbol	Function	Remarks
1	36	VCC	+3.3V Power Supply	
1	37	VCC	+3.3V Power Supply	
1	38	VCC	+3.3V Power Supply	
3	27	Vsync	Vertical Sync	Positive polarity
5	25	Hsync	Horizontal Sync	Positive polarity
6	1	GND	Ground	
7	18	B0	Blue data signal (LSB)	
8	15	G4	Green data signal	
9	3	GND	Ground	
10	16	G5	Green data signal (MSB)	
11	5	R1	Red data signal	
12	6	R2	Red data signal	
14	10	GND	Ground	
19	11	G0	Green data signal (LSB)	
20	17	GND	Ground	
21	23	B5	Blue data signal (MSB)	
23	12	G1	Green data signal	
24	22	B4	Blue data signal	
25	28	GND	Ground	
26	21	B3	Blue data signal	
27	2	DCLK	Clock signal for sampling each data signal	
28	26	DataEn / ENAB	Data Enable	
29	19	B1	Blue data signal	
30	33	GND	Ground	
31	20	B2	Blue data signal	
32	13	G2	Green data signal	
33	14	G3	Green data signal	
34	34	GND	Ground	
35	7	R3	Red data signal	
36	4	R0	Red data signal (LSB)	
38	8	R4	Red data signal	
39	39	GND	Ground	
40	9	R5	Red data signal (MSB)	
	24	GND	Ground	
	29	SCK	Clock signal for serial interface	Keep these terminals to "Hi" level
	30	SDI	Data signal for serial interface	Keep these terminals to "Hi" level
	31	WC	EEPROM write protect signal for serial interface	Keep these terminals to "Hi" level
	32	SCS	Chip select signal for serial interface	Keep these terminals to "Hi" level
	35	Reset	Reset of timing controller	
	40	GND	Ground	